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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP04/04700 03/31/2004 C-W

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003-100170 04/03/2003 C-W

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 11/17/2006

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verifier and Acknowledged Examiner's Signature Initials				

## ADDRESS

23911

## TITLE

Semiconductor device and method for manufacturing the same

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